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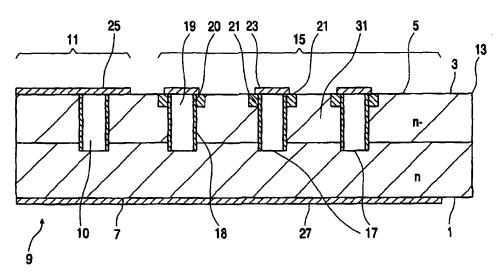
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(54) Title: SEMICONDUCTOR DEVICE WITH EDGE STRUCTURE



(57) Abstract: A semiconductor device has an edge termination region (15) having a plurality of trenches (17). Conductive material (20) and insulating material (19) is formed at the trenches, and surface implants (21) are formed on either side of the trenches. A conductive bridge (23) connects the surface implants (21) to allow equilibrium to be reached in reverse bias.

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A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H01L29/41 H01L29/06

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B. FIELDS SEARCHED

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Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

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Α	EP 1 011 146 A (ST MICROELECTRONICS SRL) 21 June 2000 (2000-06-21) cited in the application column 5, line 54 - line 57; figure 12/	1,2,5, 8-10

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European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016	Morvan, D

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